

COMMENTS AND RESPONSE

In view of the comments below, Applicants respectfully requests that the Examiner reconsider the present application including rejected claims, as amended, and withdraw the claim rejections.

Specification

The Examiner required that the Applicants update the status of the related applications, mentioned in the specification.

By this response the applicants have made the appropriate amendments to fill in all known serial numbers.

Claim Objections

The Examiner has objected to claim 4. In particular, the Examiner asked for clarification as to which element in the drawings corresponded to the claimed “self-noise suppression means” and the “detection waveform generation means.” The Examiner also noted that the word “receiver” in line 2 of claim 4 is mistyped as “reciever.” By this response, Applicants have cancelled claim 4, thus rendering moot this ground of rejection.

Claim Rejections - 35 USC § 102

The Examiner has rejected claim 4 under 35 U.S.C. § 102(e) as being allegedly anticipated by United States Patent No. 6,603,818 to Dress, Jr. et al. (“Dress”). Applicants respectfully traverse this rejection.

However in an effort to expedite prosecution, and in no way acquiescing to this rejection, Applicants have cancelled claim 4, thus rendering moot this ground of rejection.

Applicants therefore respectfully request that the Examiner withdraw the rejection of claim 4 under 35 U.S.C. § 102(e) as being allegedly anticipated by Dress.

Claim Rejections - 35 USC § 103

The Examiner has rejected claim 5 under 35 U.S.C. § 103(a) as being allegedly unpatentable over Dress in view of United States Patent No. 5,523,760 to McEwan (“McEwan”). Applicants respectfully traverse this rejection.

However in an effort to expedite prosecution, and in no way acquiescing to this rejection, Applicants have cancelled claim 5, thus rendering moot this ground of rejection.

Applicants therefore respectfully request that the Examiner withdraw the rejection of claim 5 under 35 U.S.C. § 103(a) as being allegedly unpatentable over Dress in view of McEwan.

The Examiner has rejected claim 6 under 35 U.S.C. § 103(a) as being allegedly unpatentable over Dress in view of United States Patent No. 6,112,069 to Na (“Na”). Applicants respectfully traverse this rejection.

However in an effort to expedite prosecution, and in no way acquiescing to this rejection, Applicants have cancelled claim 6, thus rendering moot this ground of rejection.

Applicants therefore respectfully request that the Examiner withdraw the rejection of claim 6 under 35 U.S.C. § 103(a) as being allegedly unpatentable over Dress in view of Na.

Allowable Claims

The Examiner has indicated that claims 1-3 and 7-11 are allowable. Applicants acknowledge that these claims are allowable.

New Claims

By this response, Applicants have added new claims 12-22. No new matter is being added with these claims. Support for new claims 12-21 can be found, for example, from page 30, line 6, through page 34, line 19, of Applicants' specification, and in Figs. 4, 5, and 6B.

In particular, the recited first stage is supported, for example, by the first mixer 31a and the code generator 31f in Fig. 4, and the analog mixer 705 and the de-jam code generator 704 in Fig. 6B, along with related disclosure in the specification.

The recited second stage is supported, for example, by the mixer 31c and the wavelet generator 31e in Fig. 4, as well as the analog mixer 708 and the wavelet generator 707 in Fig. 6B, along with related disclosure in the specification.

The recited noise suppression code, wavelet, and transmit code are supported, for example, by Code A, the wavelet stream W, and the Xmit Code shown in Fig. 5, along with related disclosure in the specification.

The recited integrator is supported, for example, by the integrator 31d in Fig. 4, along with related disclosure in the specification.

The recited multiplier is supported, for example, by the mixer 31h in Fig. 4, along with related disclosure in the specification.

The recited DC bias blocking circuit is supported, for example, by the DC blocking capacitor 31b in Fig. 4, as well as the network 706 in Fig. 6B, along with related disclosure in the specification.

Applicants respectfully request that the Examiner enter and allow new claims 12-22.

Conclusion

Accordingly, Applicants respectfully submit that the claims, as amended, clearly and patentably distinguish over the cited references of record and as such are deemed allowable. Such allowance is hereby earnestly and respectfully solicited at an early date. If the Examiner has any suggestions, comments, or questions, calls are welcome at the telephone number below.

Although it is not anticipated that any additional fees are due or payable, the Commissioner is hereby authorized to charge any fees that may be required to Deposit Account No. **50-1147**.

Respectfully Submitted,



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